

EAST - [10029582.wau...]

FileViewEditToolsWindowHelp

Drafts

Pending

Active

L2: (3238) ((address near4 transit\$3 near4 detect\$3) ATD

L3: (0) L1 and L2

L1: (24) ((sense adj2 (amp amplifier)) with (reset adj3

L4: (135) ((control\$5 adj3 circuit) with (set\$4 adj4 lat

L5: (8) L2 and L4

Failed

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DB: USPAT,US,PGPUB,EPO,JPQ,DERWENT,IBM,TOE

Default operator: OR

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BRIS Item

CLAT Item

Image

Text

HTML

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L2	3238	((address near4 transit\$3 near4 detect\$3) ATD)	USPAT	2004/04/13 13:23
2	BRS	L3	0	L1 and L2	USPAT	2004/04/13 13:23
3	BRS	L1	24	((sense adj2 (amp amplifier)) with (reset adj3 signal)) and ((control\$5 adj3 circuit) with set\$4 with latch) and ((control\$5 adj3 circuit) with reset\$4 with latch)	USPAT	2004/04/13 14:21
4	BRS	L4	135	((control\$5 adj3 circuit) with (set\$4 adj4 latch)) and ((control\$5 adj3 circuit) with (reset\$4 adj4 latch))	USPAT	2004/04/13 14:22
5	BRS	L5	8	L2 and L4	USPAT	2004/04/13 14:22

No

Details

HTML

Ready

ALUM